

Docket No.: 236518US41YA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Eric J. STRANG, et al.

SERIAL NO.: 10/673,138

GAU: 2128

FILED: September 30, 2003

EXAMINER: A. Saxena

FOR: SYSTEM AND METHOD FOR USING FIRST-PRINCIPLES SIMULATION TO
FACILITATE A SEMICONDUCTOR MANUFACTURING PROCESS

LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)

COMMISSIONER FOR PATENTS
Alexandria, VA 22313

SIR:

The following drawing sheets are submitted herewith:

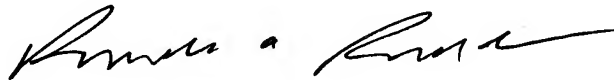
- | | |
|--|---|
| <input checked="" type="checkbox"/> <u>1</u> Replacement Drawing Sheet | <input type="checkbox"/> _____ New Drawing Sheets |
| <input type="checkbox"/> _____ Annotated Drawing Sheets | <input type="checkbox"/> _____ Color Photographs/Drawing Sheets |
|
 | |
| <input type="checkbox"/> Official Action dated _____ | |
| <input type="checkbox"/> Notice of Allowance/Issue Fee dated _____ | |
| <input type="checkbox"/> Other _____ dated _____ | |

The changes and/or modifications made include the following:

DEVICE MANUFACTURING LAB has been revised to read: DEVICE MANUFACTURING
FAB

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, L.L.P.



Philippe J.C. Signore, Ph.D.
Registration No. 43,922

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 11/09)

Ronald A. Rudder, Ph.D.
Registration No. 45,618